

DC/DC Converters



The 4:1 Input Voltage 360-Watt Single MTW DC/DC converter provides a precisely regulated dc output. The output voltage is fully isolated from the input, allowing the output to be positive or negative polarity and with various ground connections. The 360 Watt MTW meets the most rigorous performance standards in an industry standard footprint for mobile (12VIN), process control (24VIN), and military COTS (28VIN) applications.

The 4:1 Input Voltage 360 Watt MTW includes trim and remote ON/OFF. Threaded through holes are provided to allow easy mounting or addition of a heatsink for extended temperature operation.

The converters' high efficiency and high-power density are accomplished through use of high-efficiency synchronous rectification technology, advanced electronic circuit, packaging, and thermal design thus resulting in a high reliability product. Converter operates at a fixed frequency and follows conservative component de-rating guidelines.

Product is designed and manufactured in the USA.



- 4:1 Input voltage range
- · High power density
- Small size 2.4" x 2.5" x 0.52"
- · Efficiency up to 95.6%
- · Excellent thermal performance with metal case
- · Over-Current and Short Circuit Protection
- Over-Temperature protection
- · Auto-restart
- · Monotonic startup into pre-bias
- Constant frequency
- · Remote ON/OFF
- · Good shock and vibration damping
- Temperature Range -40°C to +105°C Available
- RoHS Compliant
- · UL60950 Approved

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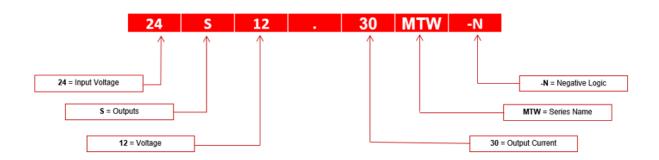
DC/DC Converters

Part Number Selection Table

1	Voltage (VDC)			Current Ripple and Efficiency Naise Requ		Regulation	Capacitive Load	Root Model		
	Input	Output	Inj	put	Output	Emolency	Noise	negulalion		nootiviouei
Vin Nom	Vin Range	Vout Nom	No Load (mA)	Max Load (A)	lo Max (A)	Typical at Max Load (%)	Typical (mVp-p)	Line / Load Max (%)	Max. C external (µF)	Basic Model without option
		12	.24	45	30	94.4	120	.05/.08	4700	24S12.30MTW
24	9 – 36	24	.24	45	15	95.2	240	.05/.08	2200	24S24.15MTW
		28	.24	50	13	95.4	280	.05/.08	2200	24S28.13MTW

- 1. Negative Logic On/Off feature is available. Add "-N" to the part number when ordering. i.e. 24S12.30MTW-N (ROHS)
- 2. Designed to meet MIL-STD-810G for functional shock and vibration. The unit must be properly secured to the interface medium (PCB/Chassis) by use of the threaded inserts of the unit.
- 3. A thermal management device, such as a heatsink, is required to ensure proper operation of this device. The thermal management medium is required to maintain baseplate < 105°C for full rated power.
- 4. Non-standard output voltages are available. Please contact the factory for additional information.

Part Number Description





DC/DC Converters

Electrical Specifications

Conditions: $T_A = 25^{\circ}C$, airflow = 300 LFM (1.5m/s), VIN = 24VDC, unless otherwise specified. Specifications subject to change without notice.

All Models					
Parameter	Notes	Min	Тур	Max	Units
Absolute Maximum Ratings	Continuous	0		40	V
Input Voltage	Transient (100ms)	U		50	V
Operating Temperature	Baseplate (100% load)	-40		105	°C
Storage Temperature	Dasopiate (10070 load)	-55		125	°C
Isolation Characteristics and Safety		33		123	0
	Input to Output	2250			V
Isolation Voltage	Input to Baseplate & Output to Baseplate	1500			V
Isolation Capacitance	input to baseplate a output to baseplate	1300	4500		pF
Isolation Resistance		10	20		MΩ
Insulation Safety Rating		10	Basic		19122
modulion outory ricking	Designed to meet UL/cUL 60950, IEC/EN	60950-1	Daoio		
Feature Characteristics	Designed to meet develor 00000, reover	00330 1			
Fixed Switching Frequency	Output Voltage Ripple has twice this frequency		200		kHz
Output Voltage Trim Range				±10	%
Remote Sense Compensation	This function is not provided		N/A		%
Output Overvoltage Protection	Non-latching	117	124	130	%
Over Temperature Shutdown (Baseplate)	Non-latching		110	120	°C
Auto-Restart Period	Applies to all protection features	450	500	550	ms
Turn-On Time from VIN	Time from UVLO to V0=90% VOUT (NOM) Resistive load		517	530	ms
Turn-On time from ON/OFF Control	Trim from ON to V0=90% VOUT (NOM) Resistive load		17	20	ms
Rise Time	VOUT from 10% to 90%	4	7.5	11	ms
ON/OFF Control – Positive Logic					
On State	Pin open = ON or external voltage applied	2		12	V
Current Control	Leakage current			0.16	mA
OFF State		0		0.8	V
Control Current	Sinking	0.3		0.36	mA
ON/OFF Control – Negative Logic		•			
ON State	Pin shorted to -INPUT or			0.8	V
OFF State	Pin open = OFF or	2		12	V
Thermal Characteristics					
Thermal resistance Baseplate to Ambient	Converter soldered to 3.95" x 2.5" x 0.07" 4 layer / 2oz copper FR4 PCB		5.2		°C/W



DC/DC Converters

Electrical Specifications (continued)

Conditions: $T_A = 25^{\circ}$ C, airflow = 300 LFM (1.5m/s), VIN = 24VDC, unless otherwise specified. Specifications are subject to change without notice.

24S12.30MTW

Departing Input Voltage Range 9 24 36 V	24S12.30MTW						
Operating Input Voltage Range September Septemb		Notes		Min	Тур	Max	Units
Input Under Voltage Loxioust Non-latching Sa.2	Input Characteristics						
Turn-on Threshold				9	24	36	V
Turn-off Threshold	Input Under Voltage Lockout	Non-latching					
Lockout Hysteresis Voltage	Turn-on Threshold			8.2	8.5	8.8	V
VIN = 9V, 80% Load	Turn-off Threshold			7.7	8	8.3	V
Maximum Input Current VIN = 12V, 100% Load John School John Sc	Lockout Hysteresis Voltage			0.4	0.55	0.7	V
No 24V Output Shorted 65 Mark Mark Mark Mark Mark Mark Mark Mark		VIN = 9V, 80% Load				45.3	А
Input Stand-by Current	Maximum Input Current	VIN = 12V, 100% Load				33.2	А
Input Current @ No Load		VIN = 24V, Output Shorted			65		mARMS
Minimum Input Capacitance (external) ESR < 0.1 Ω 470 μ μF Inrush Transient VIN = 36V (0.4V/μs) no external input cap 0.4 1 $A2s$ Input Terminal Ripple Current, i C 25 MHz bandwidth, 100% Load (Fig. 2) 560 mARMS Output Voltage Range 11.64 12.00 12.36 V Output Voltage Set Point Accuracy (50% Load) 11.88 12.00 12.12 V Output Regulation 0ver Line VIN = 9V to 36V 0.05 0.15 % Over Load VIN = 24V, Load 0% to 100% 0.08 0.15 % Temperature Coefficient 0.015 0.08 0.15 % Over Voltage Protection 14.0 15.6 V Output Ripple and Noise – 20 MHz bandwidth Fig. 3) 100% Load CEXT = 470 μF/70mΩ + 1 μF ceramic 30 60 mVRMS External Load Capacitance Full Load (resistive) -40 °C < Ta < +105 °C	Input Stand-by Current	Converter Disabled			2	4	mA
Inrush Transient VIN = 36V (0.4V/μs) no external input cap 0.4 1 A^2s marked Input Terminal Ripple Current, ℓ : 25 MHz bandwidth, 100% Load (Fig. 2) 560 mARMS Output Voltage Range 11.64 12.00 12.36 V Output Voltage Set Point Accuracy (50% Load) 11.88 12.00 12.12 V Output Regulation VIN = 9V to 36V 0.05 0.15 % Over Load VIN = 24V, Load 0% to 100% 0.08 0.15 % Temperature Coefficient 0.015 0.08 0.15 % Over Voltage Protection 14.0 15.6 V Output Ripple and Noise – 20 MHz bandwidth (Fig. 3) 100% Load CEXT = 470 μF/70mΩ + 1 μF ceramic 30 60 mVRMS External Load Capacitance Full Load (resistive) -40 °C < Ta < +105 °C	Input Current @ No Load	Converter Enabled			240	280	mA
Input Terminal Ripple Current, & 25 MHz bandwidth, 100% Load (Fig. 2) 560 mARMS	Minimum Input Capacitance (external)	ESR $< 0.1 \Omega$		470			μF
Output Voltage Range 11.64 12.00 12.36 V Output Voltage Range (50% Load) 11.88 12.00 12.12 V Output Voltage Set Point Accuracy (50% Load) 11.88 12.00 12.12 V Output Regulation Over Line VIN = 9V to 36V 0.05 0.15 % Over Load VIN = 24V, Load 0% to 100% 0.08 0.15 % Temperature Coefficient 0.015 0.03 % % Over Voltage Protection 14.0 15.6 V Output Ripple and Noise – 20 MHz bandwidth (Fig. 3) 100% Load CEXT = 470 µF/70mΩ + 1 µF ceramic 30 60 mVPRMS External Load Capacitance Full Load (resistive) -40 °C < Ta < +105 °C	Inrush Transient	VIN = 36V (0.4V/µs) no external inpu	ıt cap		0.4	1	A^2s
Output Voltage Range	Input Terminal Ripple Current, iC	25 MHz bandwidth, 100% Load (Fig	. 2)		560		mARMS
Output Voltage Set Point Accuracy (50% Load) 11.88 12.00 12.12 V Output Regulation Over Line VIN = 9V to 36V 0.05 0.15 % Over Load VIN = 24V, Load 0% to 100% 0.08 0.15 % Temperature Coefficient 0.015 0.03 %/°C Over Voltage Protection 14.0 15.6 V Output Ripple and Noise – 20 MHz bandwidth (Fig. 3) 100% Load CEXT = 470 μF/70mΩ + 1 μF ceramic 120 180 mVPK-PI External Load Capacitance Full Load (resistive) –40 °C < Ta < +105 °C	Output Characteristics						
Output Regulation VIN = 9V to 36V 0.05 0.15 % Over Load VIN = 24V, Load 0% to 100% 0.08 0.15 % Temperature Coefficient 0.015 0.03 %°°°° Over Voltage Protection 14.0 15.6 V Output Ripple and Noise − 20 MHz bandwidth (Fig. 3) 100% Load CEXT = 470 μF/70mΩ + 1 μF ceramic 120 180 mVPK-PI External Load Capacitance Full Load (resistive) -40 °C < Ta < +105 °C	Output Voltage Range			11.64	12.00	12.36	V
Over Line VIN = 9V to 36V 0.05 0.15 % Over Load VIN = 24V, Load 0% to 100% 0.08 0.15 % Temperature Coefficient 0.015 0.03 % % Over Voltage Protection 14.0 15.6 V Output Ripple and Noise – 20 MHz bandwidth (Fig. 3) 100% Load CEXT = 470 μF/70mΩ + 1 μF ceramic 120 180 mVPK-PI External Load Capacitance Full Load (resistive) - 40 °C < Ta < +105 °C	Output Voltage Set Point Accuracy	(50% Load)		11.88	12.00	12.12	V
Over Load VIN = 24V, Load 0% to 100% 0.08 0.15 % Temperature Coefficient 0.015 0.03 %/°C Over Voltage Protection 14.0 15.6 V Output Ripple and Noise – 20 MHz bandwidth (Fig. 3) 100% Load CEXT = 470 μF/70mΩ + 1 μF ceramic 120 180 mVPK-PI External Load Capacitance Full Load (resistive) -40 °C < Ta < +105 °C	Output Regulation			'	'	1	
Temperature Coefficient 0.015 0.03 %/°C Over Voltage Protection 14.0 15.6 V Output Ripple and Noise – 20 MHz bandwidth (Fig. 3) 100% Load CEXT = 470 μF/70mΩ + 1 μF ceramic 120 180 mVPK-PI External Load Capacitance Full Load (resistive) -40 °C < Ta < +105 °C	Over Line	VIN = 9V to 36V			0.05	0.15	%
Over Voltage Protection 14.0 15.6 V Output Ripple and Noise – 20 MHz bandwidth (Fig. 3) 100% Load CEXT = 470 μF/70mΩ + 1 μF ceramic 120 180 mVPK-PI External Load Capacitance Full Load (resistive) -40 °C < Ta < +105 °C	Over Load	VIN = 24V, Load 0% to 100%			0.08	0.15	%
Output Ripple and Noise – 20 MHz bandwidth (Fig. 3) 100% Load CEXT = 470 μF/70mΩ + 1 μF ceramic 120 180 mVPK-PI External Load Capacitance Full Load (resistive) -40 °C < Ta < +105 °C	Temperature Coefficient				0.015	0.03	%/°C
Output Ripple and Noise – 20 MHz bandwidth $ \begin{array}{c} \text{CEXT} = 470 \ \mu\text{F}/70\text{m}\Omega + 1 \ \mu\text{F ceramic} \\ \text{CEXT} = 470 \ \mu\text{F}/70\text{m}\Omega + 1 \ \mu\text{F ceramic} \\ \end{array} $	Over Voltage Protection			14.0		15.6	V
Output Ripple and Noise – 20 MHz bandwidth $ \begin{array}{c} \text{CEXT} = 470 \ \mu\text{F}/70\text{m}\Omega + 1 \ \mu\text{F ceramic} \\ \text{CEXT} = 470 \ \mu\text{F}/70\text{m}\Omega + 1 \ \mu\text{F ceramic} \\ \end{array} $		(Fig. 2) 100% Load			120	180	mVPK-PK
External Load Capacitance	Output Ripple and Noise – 20 MHz bandwidth	CEXT = $470 \mu F/70 \text{m}\Omega + 1 \mu F \text{ ceran}$	nic		30	60	mVRMS
ESR 10 100 mΩ Output Current Range (See Fig. A) VIN = 9V to 36V 0 30 A Current Limit Inception VIN = 9V - 36V 33 36 39 A RMS Short-Circuit Current Non-latching, Continuous 4 7 ARMS Dynamic Response Load change 50% - 75% - 50%, di/dt = 1A/μs C0 = 470 μF/70mΩ + 1 μF ceramic ± 200 ± 320 mV Load change 50% - 100% - 50%, di/dt = 1A/μs C0 = 470 μF/70mΩ + 1 μF ceramic ± 450 mV Setting Time to 1% of VOUT 400 μs Efficiency VIN = 24 V 93.7 94.4 95.1 %	Estamal Load Connectores			470		4700	μF
Current Limit Inception VIN = 9V - 36V 33 36 39 A RMS Short-Circuit Current Non-latching, Continuous 4 7 ARMS Dynamic Response Load change $50\% - 75\% - 50\%$, $di/dt = 1A/\mu s$ $C0 = 470~\mu F/70m\Omega + 1~\mu F$ ceramic ± 200 ± 320 mV Load change $50\% - 100\% - 50\%$, $di/dt = 1A/\mu s$ $C0 = 470~\mu F/70m\Omega + 1~\mu F$ ceramic ± 450 mV Setting Time to 1% of VOUT 400 μs Efficiency $VIN = 24~V$ 93.7 94.4 95.1 %	External Load Capacitance	-40 °C < 1a < +105 °C		10		100	mΩ
RMS Short-Circuit Current Non-latching, Continuous 4 7 ARMS Dynamic Response Load change 50% - 75% - 50%, di/dt = 1A/μs $C_0 = 470 \mu F/70 m\Omega + 1 \mu F$ ceramic $\pm 200 \pm 320 mV$ Load change 50% - 100% - 50%, di/dt = 1A/μs $C_0 = 470 \mu F/70 m\Omega + 1 \mu F$ ceramic $\pm 450 mV$ Setting Time to 1% of VOUT 400 μ S Efficiency VIN = 24 V 93.7 94.4 95.1 %	Output Current Range (See Fig. A)	VIN = 9V to 36V		0		30	А
Dynamic Response Load change 50% - 75% - 50%, di/dt = 1A/μs $C0 = 470 \mu F/70 m\Omega + 1 \mu F$ ceramic ± 200 ± 320 mV Load change 50% - 100% - 50%, di/dt = 1A/μs $C0 = 470 \mu F/70 m\Omega + 1 \mu F$ ceramic ± 450 mV Setting Time to 1% of VOUT 400 μS Efficiency VIN = 24 V 93.7 94.4 95.1 %	Current Limit Inception	VIN = 9V - 36V		33	36	39	А
Dynamic Response Load change 50% - 75% - 50%, di/dt = 1A/μs $C0 = 470 \mu F/70 m\Omega + 1 \mu F$ ceramic ± 200 ± 320 mV Load change 50% - 100% - 50%, di/dt = 1A/μs $C0 = 470 \mu F/70 m\Omega + 1 \mu F$ ceramic ± 450 mV Setting Time to 1% of VOUT 400 μS Efficiency 100% Load VIN = 24 V 93.7 94.4 95.1 %	RMS Short-Circuit Current	Non-latching, Continuous			4	7	ARMS
Load change 50% - 100% - 50%, di/dt = 1A/μs $C_0 = 470 \mu F/70 m\Omega + 1 \mu F$ ceramic ± 450 mV Setting Time to 1% of VOUT ± 400 μs Efficiency $VIN = 24 V$ 93.7 94.4 95.1 %	Dynamic Response			·	·		
Load change 50% - 100% - 50%, di/dt = 1A/μs C0 = 470 μF/70mΩ + 1 μF ceramic \pm 450 mV Setting Time to 1% of VOUT 400 μs Efficiency VIN = 24 V 93.7 94.4 95.1 %	Load change 50% - 75% - 50%, di/dt = 1A/µs	$CO = 470 \mu\text{F}/70\text{m}\Omega + 1 \mu\text{F ceramic}$			± 200	± 320	mV
Setting Time to 1% of VOUT 400 μs Efficiency VIN = 24 V 93.7 94.4 95.1 %	Load change 50% - 100% - 50%, di/dt = 1A/µs				± 450		mV
Efficiency VIN = 24 V 93.7 94.4 95.1 %							μs
100% Load							
100% Load		VIN = 24 V		93.7	94.4	95.1	%
	100% Load					%	
VIN = 24 V 94.1 94.8 95.5 %		VIN = 24 V		94.1		95.5	%
50% Load VIN = 12 V 94 94.7 95.1 %	50% Load	VIN = 12 V	94	94.7	95.1	%	



DC/DC Converters

Electrical Specifications (continued)

Conditions: $T_A = 25^{\circ}\text{C}$, airflow = 300 LFM (1.5m/s), VIN = 24VDC, unless otherwise specified. Specifications are subject to change without notice.

24S24.15MTW

Parameter	Notes		Min	Тур	Max	Units
Input Characteristics						
Operating Input Voltage Range			9	24	36	V
Input Under Voltage Lockout	Non-latching					
Turn-on Threshold			8.2	8.5	8.8	V
Turn-off Threshold			7.7	8	8.3	V
Lockout Hysteresis Voltage			0.4	0.55	0.7	V
Maximum Input Current	VIN = 9V, 80% Load				45	А
Maximum Input Current	VIN = 12V, 100% Load				42	Α
	VIN = 24V, Output Shorted			75		mARMS
Input Stand-by Current	Converter Disabled			2	4	mA
Input Current @ No Load	Converter Enabled			240	300	mA
Minimum Input Capacitance (external)	ESR $< 0.1 \Omega$		470			μF
Inrush Transient	VIN = 36V (0.4V/μs) no external i	nput cap		0.4	1	A ² s
Input Terminal Ripple Current, iC	25 MHz bandwidth, 100% Load (Fig. 2)		600		mARMS
Output Characteristics						
Output Voltage Range			23.28	24.00	24.72	V
Output Voltage Set Point Accuracy	(50% Load)		23.76	24.00	24.24	V
Output Regulation				1	1	
Over Line	VIN = 9V to 36V			0.05	0.15	%
Over Load	VIN = 24V, Load 0% to 100%			0.08	0.15	%
Temperature Coefficient				0.015	0.03	%/°C
Over Voltage Protection			28.1		31.2	V
O to I Birds and Naise OO Mile board Sills	(Fig. 3) 100% Load		240	360	mVPK-PK	
Output Ripple and Noise – 20 MHz bandwidth	$CEXT = 470 \mu F/70 m\Omega + 1 \mu F ce$	ramic		50	80	mVRMS
External Load Capacitance	Full Load (resistive) -40 °C < Ta < +105 °C	CEX T	470		2200	μF
		ESR	10		100	mΩ
Output Current Range (See Fig. A)	VIN = 9V to 36V		0		15	А
Current Limit Inception	VIN = 9V - 36V		16.5	18	19.5	А
RMS Short-Circuit Current	Non-latching, Continuous			3.8	6	ARMS
Dynamic Response						
Load change 50% - 75% - 50%, di/dt = $1A/\mu s$	$CO = 470 \mu F/70 mΩ + 1 \mu F$ ceramic			± 280	± 420	mV
Load change 50% - 100% - 50%, di/dt = 1A/ μ s	$C_0 = 470 \mu\text{F}/70\text{m}\Omega + 1 \mu\text{F ceramic}$			± 500		mV
Setting Time to 1% of VOUT				600		μs
Efficiency						
1000/ 1 1	VIN = 24 V		94.5	95.2	95.9	%
100% Load	VIN = 12 V		93.8	94.5	95.2	%
FOOV Load	VIN = 24 V		94.5	95.4	96.1	%
50% Load	VIN = 12 V		94.6	95.2	95.9	%



DC/DC Converters

Electrical Specifications (continued)

Conditions: $T_A = 25$ °C, Airflow = 300 LFM (1.5 m/s), Vin = 24VDC, unless otherwise specified. Specifications are subject to change without notice.

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24528.30FXW	- Notos		Min	Tun-	Max	Units
Parameter Insut Characteristics	Notes		IVIIN	Тур	IVIAX	Units
Input Characteristics			0	0.4	0.0	\ /
Operating Input Voltage Range Input Under Voltage Lockout	Non latabina		9	24	36	V
	Non-latching		0.0	0.5	0.0	1/
Turn-on Threshold			8.2	8.5	8.8	V
Turn-off Threshold			7.7	8	8.3	V
Lockout Hysteresis Voltage	VIII. 01/ 000/ I		0.4	0.55	0.7	V
	VIN = 9V, 80% Load				45	A
Maximum Input Current	VIN = 12V, 100% Load				42	Α
	VIN = 24V, Output Shorted			55		mARM S
Input Stand-by Current	Converter Disabled			2	4	mA
Input Current @ No Load	Converter Enabled			240	280	mA
Minimum Input Capacitance (external)	ESR $< 0.1 \Omega$		470	240	200	μF
Inrush Transient	VIN = $36V$ (0.4V/ μ s) no external	innut can	770	0.4	1	Α ² s
	` ' '				'	
Input Terminal Ripple Current, iC	25 MHz bandwidth, 100% Load	(FIG. 2)		560		mARM S
Output Characteristics						Ü
Output Voltage Range			27.16	28.00	28.84	V
Output Voltage Set Point Accuracy	(50% Load)		27.72	28.00	28.28	V
Output Regulation	,					
Over Line	VIN = 9V to 36V			0.05	0.15	%
Over Load	VIN = 24V, Load 0% to 100%			0.08	0.15	%
Temperature Coefficient				0.015	0.03	%/°C
Over Voltage Protection			32.8		36.4	V
	(Fig. 3) 100% Load			280	380	mVPK-
Output Ripple and Noise – 20 MHz bandwidth	(1.9. 0) 100 /0 1000					PK
	CEXT = 470 μ F/70m Ω + 1 μ F cera	mic		50	85	mVRMS
External Load Capacitance	Full Load (resistive)	CEXT	470		2200	μF
External Educ Supusitanos	-40 °C < Ta < +105 °C	ESR	10		100	mΩ
Output Current Range (See Fig. A)	VIN = 9V to 36V		0		13	Α
Current Limit Inception	VIN = 9V - 36V		14.3	15.6	16.9	Α
RMS Short-Circuit Current	Non-latching, Continuous			2.2	6	ARMS
Dynamic Response						
Load change 50% - 75% - 50%, $di/dt = 1A/\mu s$	$C_0 = 470 \ \mu\text{F}/70 \text{m}\Omega + 1 \ \mu\text{F ceramic}$			± 180	± 300	mV
Load change 50% - 100% - 50%, di/dt = 1A/ μ s	$C_0 = 470 \ \mu\text{F}/70\text{m}\Omega + 1 \ \mu\text{F ceramic}$			± 400		mV
Setting Time to 1% of VOUT				500		μs
Efficiency						
100% Load	VIN = 24 V		94.3	95.4	96.1	%
100% Load	VIN = 12 V		93.7	94.4	95.1	%
50% Load	VIN = 24 V		94.3	95.0	95.7	%
JO /U LUQU	VIN = 12 V		94.0	94.7	95.1	%
	·					



Environmental and Mechanical Specifications

Specifications are subject to change without notice

Parameter	Note	Min	Тур	Max	Units		
Environmental							
Operating Humidity	Non-condensing			95	%		
Storage Humidity	Non-condensing			95	%		
ROHS Compliance1	See Calex Website http://www.calex.com/RoHS.ht	ml for the con	nplete RoHS C	compliance S	tatement		
Shock and Vibration	Designed to meet MIL-STD-810G for functional sh	ock and vibra	tion				
Water Washability	Not recommended for water wash process. Contac	lot recommended for water wash process. Contact the factory for more information.					
Mechanical							
Woight		3.85					
Weight			109.2		Grams		
PCB							
Operating Temperature				130	°C		
Tg		170			°C		
	Pins 1 ,4, 5 and 9	0.079	0.081	0.083	Inches		
Through Holo Din Diamotoro	1 1115 1 ,4, 5 and 9	2.006	2.057	2.108	mm		
Through Hole Pin Diameters	Pins 3 and 7	0.038	0.04	0.042	Inches		
		0.965	1.016	1.067	mm		
Through Hole Pin Material	Pins 1, 4, 5 and 9	C1	4500 or C11	00 Copper A	Alloy		
Tillough Hole Fill Material	Pins 3 and 7	В	rass Alloy TB:	3 or "Eco Bra	iss"		
Through Hole Pin Finish	All pins		10µ" Gold	over Nickel			
Case Dimensions		2	2.4 x 2.5 x 0.5	52	Inches		
Case Difficilisions		60.9	96 x 63.50 x	13.21	mm		
Case Material	Plastic: Vectra LCP FIT30: ½ - 16 EDM Finish						
	Material		Alum	inum			
Baseplate	Flatness		0.008		Inches		
	Tiatiless		0.20		mm		
Reliability							
MTBF	Telcordia SR-332, Method 1 Case 1 50% electrical stress, 40 °C components	5.4 MHr			MHrs		
Agency Approvals	UL60950						
EMI and Regulatory Compliance							
Conducted Emissions	MIL-STD-461F CE102 with external EMI filter netw	vork (see Figs	s, 28 and 29)				

Additional Notes:

1 The RoHS marking is: (Pb)



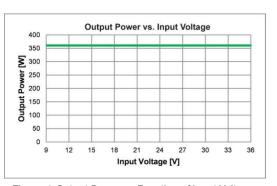
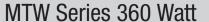


Figure 1: Output Power as Function of input Voltage.





Operations

Input and Output Capacitance

In many applications, the inductance associated with the distribution from the power source to the input of the converter can affect the stability of the converter. This becomes of great consideration for input voltage at 12V or below. To enable proper operation of the converter, in particular during load transients, an additional input capacitor is required. Minimum required input capacitance, mounted close to the input pins, is $1000\mu F$ with ESR $<0.1~\Omega$. Since inductance of the input power cables could have significant voltage drop due to rate of change of input current di(in)/dt during transient load operation an external capacitance on the output of the converter is required to reduce di(in)/dt. It is required to use at least 470 μF (ESR $<0.07\Omega$) on the output.

Another constraint is minimum rms current rating of the input and output capacitors which is application dependent. One component of input rms current handled by input capacitor is high frequency component at switching frequency of the converter (typ. 400kHz) and is specified under "Input terminal ripple current" ic. Typical values at full rated load and 24 Vin are provided in Section "Characteristic Waveforms" for each model and are in range of 0.56A - 0.6A. The second component of the ripple current is due to reflected step load current on the input of the converter. Similar consideration needs to be taken for the output capacitor and step load ripple current component. Consult the factory for further application guidelines.

Additionally, for EMI conducted measurement it is necessary to use $5\mu H$ LISNs instead of typical $50\mu H$ LISNs.

ON/OFF (Pin 3)

The ON/OFF pin is used to turn the power converter on or off remotely via a system signal and has positive logic. A typical connection for remote ON/OFF function is shown in Figure 2.

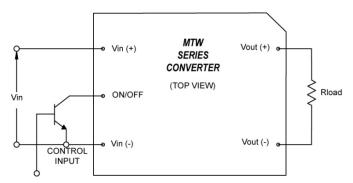


Figure 2: Circuit Configuration for ON/OFF Function

The positive logic version turns on when the ON/OFF pin is at logic high and turns off when at logic low. The converter is on when the ON/OFF pin is either left open or external voltage not more than 12V is applied between ON/OFF pin and -INPUT pin. See the Electrical Specifications for logic high/low definitions.

The negative logic version turns on when the ON/OFF pin is at logic low and turns off when at logic high. The converter is on when the ON/OFF pin is either shorted to -INPUT pin or kept below 0.8V. The converter is off when the ON/OFF pin is either left open or external voltage greater than 2V and not more than 12V is applied between ON/OFF pin and INPUT pin. See the Electrical Specifications for logic high/ low definitions.

The ON/OFF pin is internally pulled up to typically 4.5V via resistor and connected to internal logic circuit via RC circuit in order to filter out noise that may occur on the ON/OFF pin. A properly debounced mechanical switch, open-collector transistor, or FET can be used to drive the input of the ON/OFF pin. The device must be capable of sinking up to 0.36mA at a low level voltage of \leq 0.8V. During logic high, the typical maximum voltage at ON/OFF pin (generated by the converter) is 4.5V, and the maximum allowable leakage current is 160µA. If not using the remote on/off feature leave the ON/OFF pin open.

TTL Logic Level - The range between 0.81V as maximum turn off voltage and 2V as minimum turn on voltage is considered the deadband. Operation in the dead-band is not recommended.

External voltage for ON/OFF control should not be applied when there is no input power voltage applied to the converter.



DC/DC Converters

Protection Features

Input Undervoltage lockout (UVLO)

Input undervoltage lockout is standard with this converter. The converter will shut down when the input voltage drops below a predetermined voltage.

The input voltage must be typically above 8.5V for the converter to turn on. Once the converter has been turned on, it will shut off when the input voltage drops typically below 8V. If the converter is started by input voltage (ON/ OFF (pin 3) left open) there is typically 500msec delay from the moment when input voltage is above 8.5V turn-on voltage and the time when output voltage starts rising. This delay is intentionally provided to prevent potential startup issues especially at low input voltages.

Output Overcurrent Protection (OCP)

The converter is protected against overcurrent or short circuit conditions. Upon sensing an overcurrent condition, the converter will switch to constant current operation and thereby begin to reduce output voltage. When the output voltage drops below approx. 75% of the nominal

value of output voltage, the converter will shut down.

Once the converter has shut down, it will attempt to restart nominally every 500msec with a typical 3% duty cycle. The attempted restart will continue indefinitely until the overload or short circuit conditions are removed or the output voltage rises above 75% of its nominal value.

Once the output current is brought back into its specified range, the converter automatically exits the hiccup mode and continues normal operation.

During initial startup, if output voltage does not exceed typical 75% of nominal output voltage within 20 msec after the converter is enabled, the converter will be shut down and will attempt to restart after 500 msec.

Output Overvoltage Protection (OVP)

The converter will shut down if the output voltage across VOUT (+) (Pin 5) and VOUT (-) (Pin 9) exceeds the threshold of the OVP circuitry. The OVP circuitry contains its own reference, independent of the output voltage regulation loop. Once the converter has shut down, it will attempt to restart every 500 msec until the OVP condition is removed.

Over Temperature Protection (OTP)

The MTW converters have non-latching over temperature protection. It will shut down and disable the output if temperature at the center of the base place exceeds a threshold of 114°C (typical).

The converter will automatically restart when the base temperature has decreased by approximately 20°C.

Safety Requirements

Basic Insulation is provided between input and the output.

The converters have no internal fuse. To comply with safety agency requirements, a fast-acting or time-delay fuse is to be provided in the unearthed lead.

Recommended fuse values are:

- a) 50A for 9V < VIN < 18V
- b) 25A for 18V < VIN < 36V

Electromagnetic Compatibility (EMC)

EMC requirements must be met at the end-product system level, as no specific standards dedicated to EMC characteristics of board mounted component dc-dc converters exist.

With the addition of a single stage external filter, the MTW converters will pass the requirements of MIL-STD-461F CE102 Base Curve for conducted emissions.

Absence of the Remote Sense Pins

Customers should be aware that MTW converters do not have a Remote Sense feature. Care should be taken to minimize voltage drop on the user's motherboard as well as if trim function is used.

Output Voltage Adjust/TRIM (Pin 7)

The TRIM pin allows user to adjust output voltage 10% up or down relative to rated nominal voltage by addition of external trim resistor. Due to absence of Remote Sense Pins, an external trim resistor should be connected to output pins using Kelvin connection. If trimming is not used, the TRIM pin should be left open.



DC/DC Converters

Trim Down - Decrease Output Voltage

Trimming down is accomplished by connecting an external resistor, $R_{\text{TRIM-DOWN}}$, between the TRIM (pin 7) and the Vout- (pin 9), using A Kelvin connection with a value of:

$$R_{TRIM-DOWN} = \left(\frac{3010}{\Delta} - 60.2\right) = [k\Omega]$$

where,

 $R_{trim-down} = Required value of the trim-down resistor [k\Omega]$

 $V_{O(NOM)}$ = Nominal value of output voltage [V]

 $V_{O(REQ)} = Required value of output voltage [V]$

$$\Delta = \left| \frac{Vo_{(req.)} - Vo_{(nom.)}}{Vo_{(nom)}} \right| [\%]$$

To trim the output voltage 10% (Δ =10) down, required external trim resistance is:

$$R_{(Trim-Down)} = \left(\frac{3010}{10} - 60.2\right) = 240.8k\Omega$$

Trim Up – Increase Output Voltage

Trimming up is accomplished by connecting an external resistor, $R_{TRIM-UP}$, between the TRIM (pin 7) and the VOUT(+) (pin 5), with a value of:

$$R_{(Trim-Up)} = 30.1 \times \left[\frac{V_{o(nom.)} \times (100 + \Delta)}{1.225 \times \Delta} - \frac{100 + 2\Delta}{\Delta} \right] \left[k\Omega \right]$$

To trim the output voltage up (for example 24V to 26.4V), Δ =10 and required external resistor is:

$$R_{(Trim-Up)} = 30.1 \times \left[\frac{24 \times (100 + 10)}{1.225 \times 10} - \frac{100 + (2 \times 10)}{10} \right] = 6125 [k\Omega]$$

Note that trimming output voltage more than 10% is not recommended and OVP might be tripped.

Active Voltage Programming

In applications where output voltage needs to be adjusted actively, an external voltage source (for example a Digital-to-Analog converter), capable of both sourcing and sinking current can be used. It should be connected with series resister Rg across TRIM (pin 7) and VOUT(-) (pin 9) using Kelvin connection. Contact a Calex technical representative for more details.

Thermal Consideration

The MTW converter can operate in a variety of thermal environments. However, to ensure reliable operation of the converter, sufficient cooling should be provided. The MTW converter is encapsulated in plastic case with metal baseplate on the top. To improve thermal performance, power components inside the unit are thermally coupled to the baseplate. In addition, thermal design of the converter is enhanced by use of input and out pins as heat transfer elements. Heat is removed from the converter by conduction, convection and radiation.

There are several factors such as ambient temperature, airflow, converter power dissipation, converter orientation of how the converter is mounted, and the need for increased reliability, which needs to be considered to achieve required performance. It is recommended to measure temperature in the middle of the baseplate in particular application to ensure that proper cooling of the convert is provided.

A reduction in the operating temperature of the converter results in increased reliability.

Thermal Derating

There are two most common applications: 1) the MTW converter is thermally attached to a cold plate inside chassis without any forced internal air circulation; 2) the MTW converter is mounted in an open chassis on system board with forced airflow with or without an additional heatsink attached to the baseplate of the MTW converter.

The best thermal results are achieved in application 1) since the converter is cooled entirely by conduction of heat from the top surface of the converter to a cold plate and temperature of the components is determined by the temperature of the cold plate. There is also additional heat removal through the converters pins to the metal layers in the system board. It is highly recommended to solder pins to the system board rather than using receptacles. Typical derating output power and current are shown in Figs. 10–15 for various baseplate temperatures up to 105°C. The converter was solder to the test card:

4.26" x 5.9" 4 layers FR4 PCB with 30z Cu inner layers and 2 Oz Cu outer layers, covered with solder mask. Note that operating converter at these limits for prolonged time affects reliability.



DC/DC Converters

Soldering Guidelines

The ROHS-compliant through hole MTW converters use Sn/Ag/Cu Pb-free solder and ROHS compliant components. They are designed to be processed through wave soldering machines. The pins are 100% matte tin over nickel plated and compatible with both Pb and Pb-free wave soldering processes. Follow these specifications when installing and soldering MTW converters. Exceeding these specifications may cause damage to the MTW converter.

Wave Solder Guideline for Sn/Ag/Cu based solders	
Maximum Preheat Temperature	115 °C
Maximum Pot Temperature	270 °C
Maximum Solder Dwell Time	7 seconds

Wave Solder Guideline for SN/Pb based solders				
Maximum Preheat Temperature	105 °C			
Maximum Pot Temperature	250 °C			
Maximum Solder Dwell Time	6 seconds			

MTW converters are not recommended for water wash process. Contact the factory for additional information if water wash is necessary.

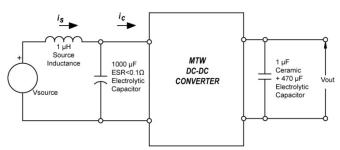


Figure 3: Test setup for measuring input reflected ripple currents $i_{\mathbb{C}}$ and $i_{\mathbb{S}}$.

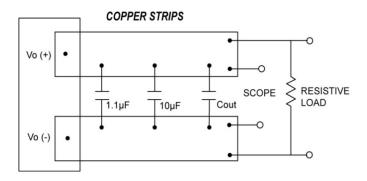
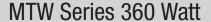


Figure 4: Test setup for measuring output voltage ripple, startup and step load transient waveforms





Characteristic Curves – Efficiency and Power Dissipation

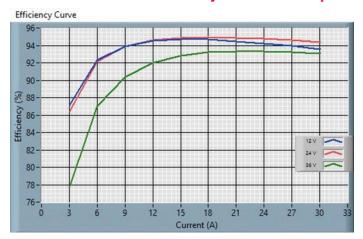


Figure 5: 24S12.30MTW (ROHS) Efficiency Curve

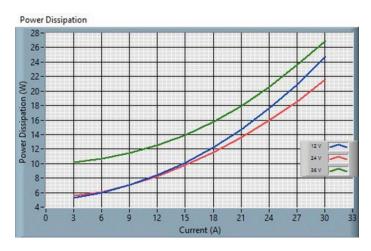


Figure 6: 24S12.30MTW (ROHS) Power Dissipation

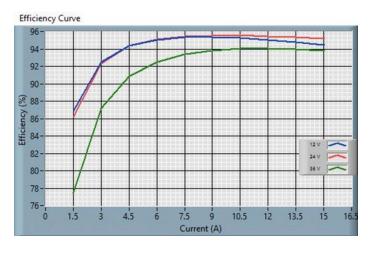


Figure 7: 24S24.15MTW (ROHS) Efficiency Curve

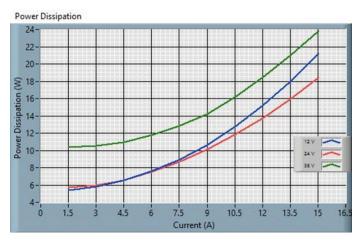


Figure 8: 24S24.15MTW (ROHS) Power Dissipation

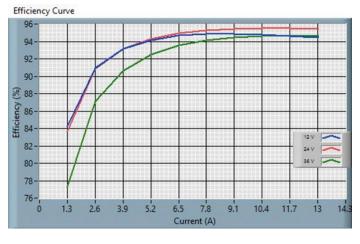


Figure 9: 24S28.13MTW (ROHS) Efficiency Curve

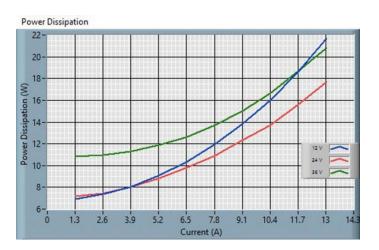
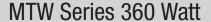


Figure 10: 24S28.13MTW (ROHS) Power Dissipation





Characteristic Curves – Derating vs. Baseplate Temperature

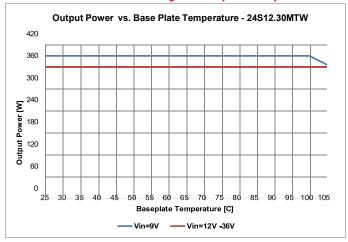


Figure 11: 24S12.30MTW (ROHS) Derating Curve

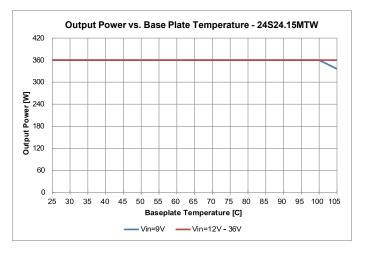


Figure 13: 24S24.15MTW (ROHS) Derating Curve

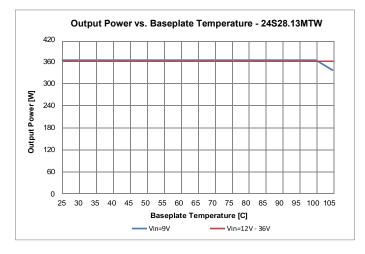


Figure 15: 24S28.13MTW (ROHS) Derating Curve

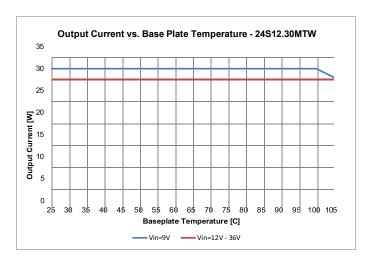


Figure 12: 24S12.30MTW (ROHS) Derating Curve

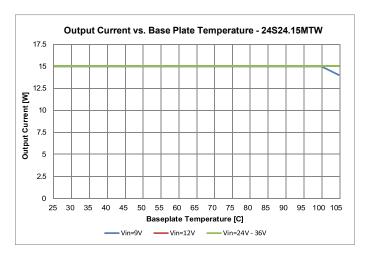


Figure 14: 24S24.15MTW (ROHS) Derating Curve

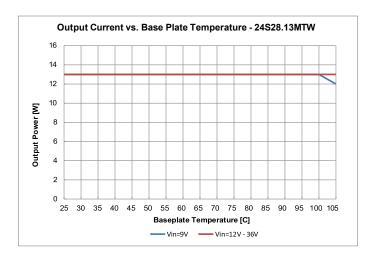
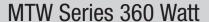


Figure 16: 24S28.13MTW (ROHS) Derating Curve





Characteristic Waveforms - 24S12.30MTW (ROHS)

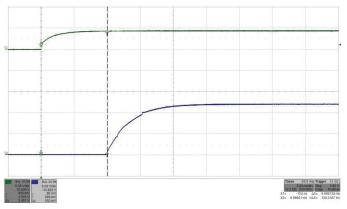


Figure 17: Turn-on by ON/OFF transient (with VIN applied) at full rated load current (resistive) at VIN = 24V. Top trace (C1): ON/OFF signal (5V/div.). Bottom trace (C4): Output voltage (5 V/div.). Time 5 ms/div.

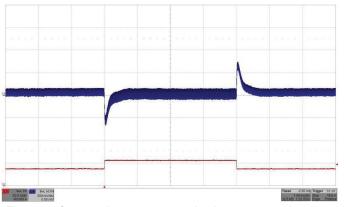


Figure 19: Output voltage response to load current step change 50% -75% - 50% (15A -22.5A - 15A) with di/dt $= 1A/\mu s$ at VIN = 24V. Top trace (C4): Output voltage (200 mV/div.). Bottom trace (C3): Load current (20A/div.). $C_0 470\mu F/70m\Omega$. Time: 1ms/div.

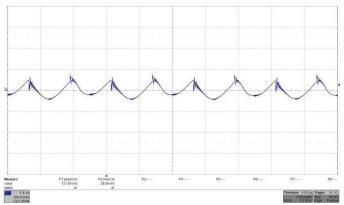


Figure 21: Output voltage ripple (100mv/div.) at full rated load current into a resistive load at VIN = 24V. C_0 470 μ F/70m Ω . Time: 2μ s/div.

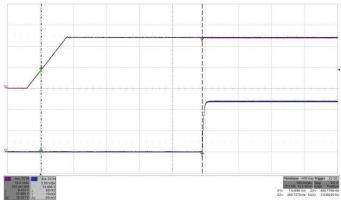


Figure 18: Turn-on by VIN (ON/OFF high) transient at full rated load current (resistive) at VIN = 24V. Top trace (C2): Input voltage VIN (10 V/div.). Bottom trace (C4): Output voltage (5 V/div.). Time 100 ms/div.

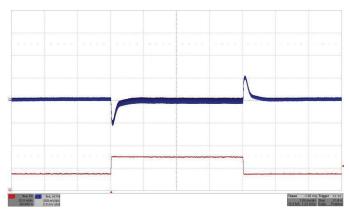


Figure. 20: Output voltage response to load current step change 50% - 100% - 50% (15A-30A-15A) with di/dt = $1A/\mu s$ at VIN = 24V. Top trace (C4): Output voltage (500 mV/div.). Bottom trace (C3): Load current (20A/div.). C_0 $470\mu F/70m\Omega$. Time: 1ms/div.

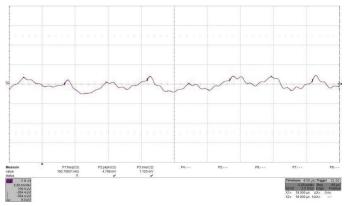
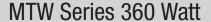


Figure 22: Input reflected ripple current, iC (500 mA/mV), measured at input terminals at full rated load current at VIN = 24V. Refer to Fig. 2 for test setup. Time: $2 \mu s/div$. RMS input ripple current is 1.125*500mA = 560mA.





Characteristic Waveforms - 24S24.15MTW (ROHS)

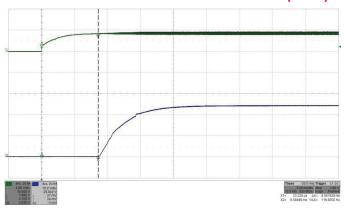


Figure 23: Turn-on by ON/OFF transient (with VIN applied) at full rated load current (resistive) at VIN = 24V. Top trace (C1): ON/OFF signal (5V/div.). Bottom trace (C4): Output voltage (10 V/div.). Time 5 ms/div.

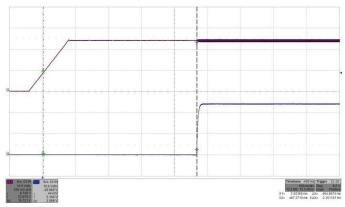


Figure 24: Turn-on by VIN transient (ON/OFF high) at full rated load current (resistive) at VIN = 24V. Top trace (C2): Input voltage VIN (10 V/div.). Bottom trace (C4): Output voltage (10 V/div.). Time 100 ms/div.

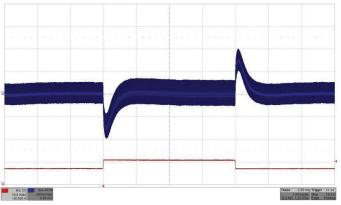


Figure 25: Output voltage response to load current step change 50% -75% - 50% (7.5A -11.25A -7.5A) with di/ dt = 1A/µs at VIN = 24V. Top trace (C4): Output voltage (200 mV/div.). Bottom trace (C3): Load current (10A/div.). $C_0 \, 470\mu F/70m\Omega. \, \text{Time: 1ms/div.}$

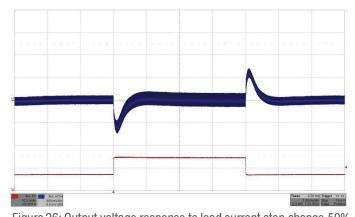


Figure 26: Output voltage response to load current step change 50% - 100% - 50% (7.5A - 15A - 7.5A) with di/dt = $1A/\mu s$ at VIN = 24V. Top trace (C4): Output voltage (500 mV/div.). Bottom trace (C3): Load current (10A/div.). C_0 $470\mu F/70m\Omega$. Time: 1ms/div.

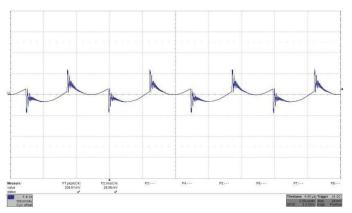


Figure 27: Output voltage ripple (200mv/div.) at full rated load current into a resistive load at VIN = 24V. $C_0\,470\mu\text{F}/70\text{m}\Omega.\text{ Time: }2\mu\text{s}/\text{div.}$

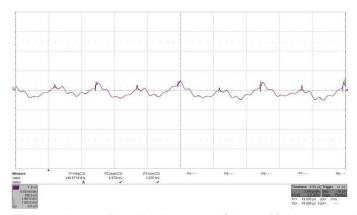
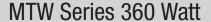


Figure 28: Input reflected ripple current, iC (500 mA/mV), measured at input terminals at full rated load current at VIN = 24V. Refer to Fig. 2 for test setup. Time: $2 \mu s$ /div. RMS input ripple current is 1.205*500 mA = 602.5 mA.





Characteristic Waveforms - 24S28.13MTW (ROHS)

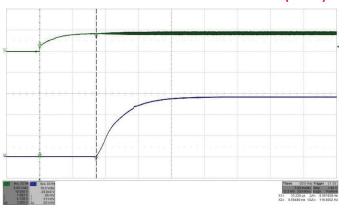


Figure 29: Turn-on by ON/OFF transient (with VIN applied) at full rated load current (resistive) at VIN = 24V. Top trace (C1): ON/OFF signal (5V/div.). Bottom trace (C4): Output voltage (10 V/div.). Time 5 ms/div.

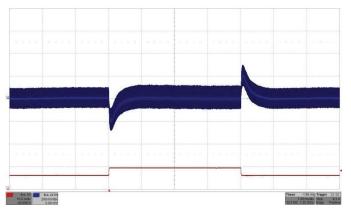


Figure 31: Output voltage response to load current step change 50% -75% - 50% (6.5A -9.75A - 6.5A) with di/dt $= 1A/\mu s$ at VIN = 24V. Top trace (C4): Output voltage (200 mV/div.). Bottom trace (C3): Load current (10A/div.). C_0 470 μ F/70m Ω . Time: 1ms/div.

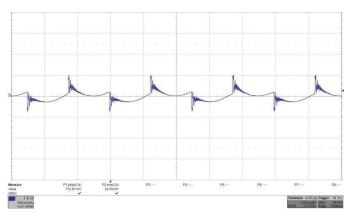


Figure 33: Output voltage ripple (200mv/div.) at full rated load current into a resistive load at VIN = 24V. C_0 470 μ F/70m Ω . Time: 2 μ s/div.

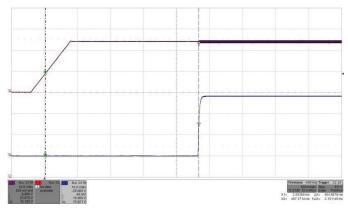


Figure 30: Turn-on by VIN transient (ON/OFF high) at full rated load current (resistive) at VIN = 24V. Top trace (C2): Input voltage VIN (10 V/div.). Bottom trace (C4): Output voltage (10 V/div.). Time 100 ms/div.

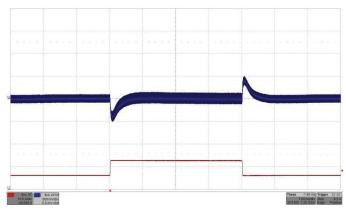


Figure 32: Output voltage response to load current step change 50% - 100% - 50% (6.5A - 13A - 6.5A) with di/dt = $1A/\mu s$ at VIN = 24V. Top trace (C4): Output voltage (500 mV/div.). Bottom trace (C3): Load current (10A/div.). C_0 $470\mu F/70m\Omega$. Time: 1ms/div.

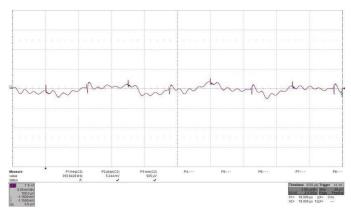


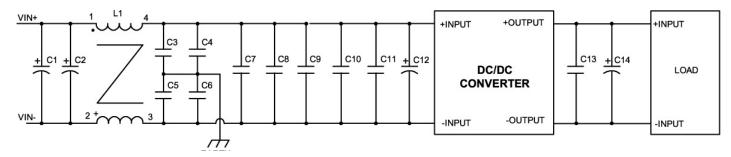
Figure 34: Input reflected ripple current, iC (500 mA/mV), measured at input terminals at full rated load current at VIN = 24V. Refer to Fig. 2 for test setup. Time: $2 \mu s/div$. RMS input ripple current is 0.935*500mA = 549mA.



EMC Considerations

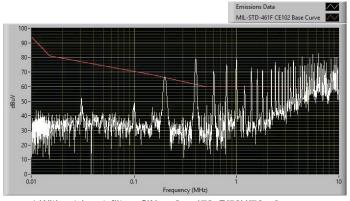
The filter schematic for suggested input filter configuration as tested to meet the conducted emission limits of MIL-STD- 461F CE102 Base Curve is shown in Figure 35. The plots of conducted EMI spectrum are shown in Figure 36.

Note: The customer is responsible for the proper selection, component rating and verification of the suggested parts based on the end application.

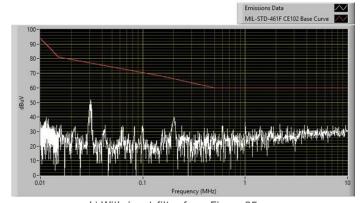


Comp. Des.	Description
C1, C2, C12, C14	470μF/50V/70mΩ Electrolytic Capacitor (Vishay MAL214699108E3 or equivalent)
C3, C4, C5, C6	4.7nF/1210/X7R/1500V Ceramic Capacitor
C7, C8, C9, C10, C11, C13	10μF/1210/X7R/50V Ceramic Capacitor
L1	CM choke: $L=130\mu H$, Llkg = $0.6\mu H$ (4 turns on toroid 22.1mm x 13.7mm x 7.92mm)

Figure 35: Typical input EMI filter circuit to attenuate conducted emissions per MIL-STD-461F CE102 Base Curve

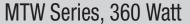






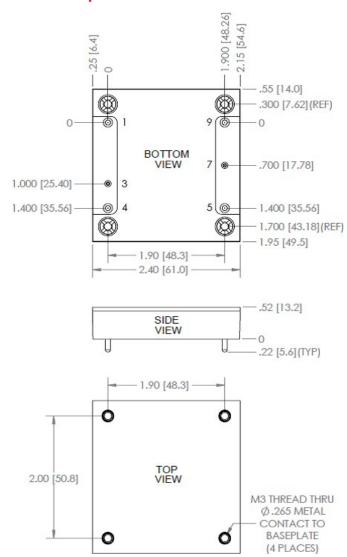
b) With input filter from Figure 35

Figure 36: Input conducted emissions measurement (Typ.) of 24S24.15MTW (ROHS)





Mechanical Specifications



Notes:

- Pinout is inconsistent between manufacturers of the half brick converters. Ensure to follow the pin function, the pin number, when laying out the board.
- 2) Pin diameter for the input pins of the MTW converters has diameter 0.081" due to high current at low line and is different from other manufacturers of the half brick. Ensure to follow pin dimensions in your application.

Input Output Connections

Pin	Name	Function
1	-INPUT	Negative input voltage
3	ON/OFF	TTL input with internal pull up, referenced to –INPUT, used to turn converter on and off
4	+INPUT	Positive input voltage
5	+0UTPUT	Positive output voltage
7	TRIM	Output voltage trim
9	-OUTPUT	Negative output voltage

Notes:

Unless otherwise specified: All dimensions are in inches [millimeters] Tolerances: x.xx in. ± 0.02 in [x.x mm ± 0.5 mm]

x.xxx in. ± 0.010 in [x.xx mm ± 0.25 mm]

Torque fasteners into threaded mounting inserts at 10in.lbs. or less. Greater torque may result in damage to unit and void the warranty.

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